

PN	Core	Cordic	HCL	FFT	PFM	DCE	GPT	Memory		Signal Chain			Control device								Interface								GPIO	Package	Operating Temperature Range(Tj)	Package qty	
SRAM	FLASH	ADC	PGA	DAC	HRPWM/PWM	CAP	QEP	QOUT	CPM	SDFM	TA-IF/BIS-IF /EDAT-IF	PBUS	ESC	PHY	USB	EMAC	UART	QSPI	SPI	I ² C	CAN												
M76P00																																	
M76P08SGK S	2x(RISC-V 552MHz+FPU+DSP)	x1	x2	Y	Y	Y	x4	1MB	2MB	12bit 4MSPs x2/24CH 14bit 2MSPs x2/24CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	Y	N	x1	x1	x9	x3	x5	x4	CAN x1 CAN-FD x2	150	BGA196 12x12mm,0.8mm	-40~125℃	盘168 包1680
M76P06SPH S	2x(RISC-V 552MHz+FPU+DSP)	x1	x2	Y	Y	Y	x4	1MB	2MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	Y	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	122	eLQFP144 20x20mm,0.5mm	-40~125℃	盘60 包600
M76P06SGH S	2x(RISC-V 552MHz+FPU+DSP)	x1	x2	Y	Y	Y	x4	1MB	2MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	Y	N	x1	x1	x8	x2	x4	x4	CAN x1 CAN-FD x2	118	BGA144 10x10mm,0.8mm	-40~125℃	盘240 包2400
M73P00																																	
M73P06SPH S	1x(RISC-V 552MHz+FPU+DSP)	x1	x1	Y	Y	Y	x4	512KB	1MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	Y	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	122	eLQFP144 20x20mm,0.5mm	-40~125℃	盘60 包600
M73P06SGH S	1x(RISC-V 552MHz+FPU+DSP)	x1	x1	Y	Y	Y	x4	512KB	1MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	Y	N	x1	x1	x8	x2	x4	x4	CAN x1 CAN-FD x2	118	BGA144 10x10mm,0.8mm	-40~125℃	盘240 包2400
M7600																																	
M7608SGKS	2x(RISC-V 552MHz+FPU+DSP)	x1	x2	Y	Y	Y	x4	1MB	2MB	12bit 4MSPs x2/24CH 14bit 2MSPs x2/24CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	x1	x1	x9	x3	x5	x4	CAN x1 CAN-FD x2	150	BGA196 12x12mm,0.8mm	-40~125℃	盘168 包1680
M7606SPHS	2x(RISC-V 552MHz+FPU+DSP)	x1	x2	Y	Y	Y	x4	1MB	2MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	122	eLQFP144 20x20mm,0.5mm	-40~125℃	盘60 包600
M7606SGHS	2x(RISC-V 552MHz+FPU+DSP)	x1	x2	Y	Y	Y	x4	1MB	2MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	x1	x1	x8	x2	x4	x4	CAN x1 CAN-FD x2	118	BGA144 10x10mm,0.8mm	-40~125℃	盘240 包2400
M7605SPES	2x(RISC-V 552MHz+FPU+DSP)	x1	x2	Y	Y	Y	x4	1MB	2MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	x1	N	x9	x2	x4	x4	CAN x1 CAN-FD x2	80	eLQFP100 14x14mm,0.5mm	-40~125℃	盘90 包900
M7605SNES	2x(RISC-V 552MHz+FPU+DSP)	x1	x2	Y	Y	Y	x4	1MB	2MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	85	QFN100 12x12mm,0.4mm	-40~125℃	盘168 包1680
M7300																																	
M7305SPES	1x(RISC-V 552MHz+FPU+DSP)	x1	x1	Y	Y	Y	x4	512KB	1MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	x1	N	x9	x2	x4	x4	CAN x1 CAN-FD x2	80	eLQFP100 14x14mm,0.5mm	-40~125℃	盘90 包900
M7305SNES	1x(RISC-V 552MHz+FPU+DSP)	x1	x1	Y	Y	Y	x4	512KB	1MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	N	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	85	QFN100 12x12mm,0.4mm	-40~125℃	盘168 包1680
M7303SPCS	1x(RISC-V 552MHz+FPU+DSP)	x1	x1	Y	Y	Y	x4	512KB	1MB	12bit 4MSPs x2/13CH 14bit 2MSPs x2/13CH	x4	N	9CH/9CH	x6	x6	x1	x2	x4	x1/x1/x1	N	N	N	x1	N	x9	x2	x4	x4	CAN x1 CAN-FD x1	47	eLQFP64 10x10mm,0.5mm	-40~125℃	盘160 包1600
M7303SNCS	1x(RISC-V 552MHz+FPU+DSP)	x1	x1	Y	Y	Y	x4	512KB	1MB	12bit 4MSPs x2/15CH 14bit 2MSPs x2/15CH	x4	N	12CH/12CH	x6	x7	x1	x2	x4	x1/x1/x1	N	N	N	x1	N	x8	x2	x4	x4	CAN x1 CAN-FD x2	54	QFN68 7x7mm,0.35mm	-40~125℃	盘260 包2600
M76E00																																	
M76E08SGK S	2x(RISC-V 552MHz+FPU+DSP)	x1	N	Y	Y	Y	x4	1MB	2MB	12bit 4MSPs x2/24CH 14bit 2MSPs x2/24CH	x4	N	24CH/24CH	x6	x10	N	x2	x4	N	Y	N	N	x1	x1	x9	x3	x5	x4	CAN x1 CAN-FD x2	150	BGA196 12x12mm,0.8mm	-40~125℃	盘168 包1680
M76E06SPH S	2x(RISC-V 552MHz+FPU+DSP)	x1	N	Y	Y	Y	x4	1MB	2MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	N	x2	x4	N	Y	N	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	122	eLQFP144 20x20mm,0.5mm	-40~125℃	盘60 包600
M76E06SGH S	2x(RISC-V 552MHz+FPU+DSP)	x1	N	Y	Y	Y	x4	1MB	2MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	N	x2	x4	N	Y	N	N	x1	x1	x8	x2	x4	x4	CAN x1 CAN-FD x2	118	BGA144 10x10mm,0.8mm	-40~125℃	盘240 包2400
M76E05SPE S	2x(RISC-V 552MHz+FPU+DSP)	x1	N	Y	Y	Y	x4	1MB	2MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	N	x2	x4	N	Y	N	N	x1	N	x9	x2	x4	x4	CAN x1 CAN-FD x2	80	eLQFP100 14x14mm,0.5mm	-40~125℃	盘90 包900
M76E05SNE S	2x(RISC-V 552MHz+FPU+DSP)	x1	N	Y	Y	Y	x4	1MB	2MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	N	x2	x4	N	Y	N	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	85	QFN100 12x12mm,0.4mm	-40~125℃	盘168 包1680
M73E00																																	
M73E05SPE S	1x(RISC-V 552MHz+FPU+DSP)	x1	N	Y	Y	Y	x4	512KB	1MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	N	x2	x4	N	Y	N	N	x1	N	x9	x2	x4	x4	CAN x1 CAN-FD x2	80	eLQFP100 14x14mm,0.5mm	-40~125℃	盘90 包900
M73E05SNE S	1x(RISC-V 552MHz+FPU+DSP)	x1	N	Y	Y	Y	x4	512KB	1MB	12bit 4MSPs x2/16CH 14bit 2MSPs x2/16CH	x4	N	24CH/24CH	x6	x10	N	x2	x4	N	Y	N	N	x1	x1	x9	x2	x4	x4	CAN x1 CAN-FD x2	85	QFN100 12x12mm,0.4mm	-40~125℃	盘168 包1680
M73E03SPC S	1x(RISC-V 552MHz+FPU+DSP)	x1	N	Y	Y	Y	x4	512KB	1MB	12bit 4MSPs x2/13CH 14bit 2MSPs x2/13CH	x4	N	9CH/9CH	x6	x6	N	x2	x4	N	N	N	N	x1	N	x9	x2	x4	x4	CAN x1 CAN-FD x1	47	eLQFP64 10x10mm,0.5mm	-40~125℃	盘160 包1600
M73E03SNC S	1x(RISC-V 552MHz+FPU+DSP)	x1	N	Y	Y	Y	x4	512KB	1MB	12bit 4MSPs x2/15CH 14bit 2MSPs x2/15CH	x4	N	12CH/12CH	x6	x7	N	x2	x4	N	N	N	N	x1	N	x8	x2	x4	x4	CAN x1 CAN-FD x2	54	QFN68 7x7mm,0.35mm	-40~125℃	盘260 包2600